

AMENDMENT TO CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously presented) A graphics accelerator for processing a graphical image, the graphics accelerator comprising:
 - a single texture buffer for storing texture maps and data relating to the texture maps stored in the texture buffer; and
 - a plurality of texture processors that perform texturing operations on the graphical image, the plurality of the texture processors retrieving texture packets from the single texture buffer,each texture processor including a fetching engine that retrieves the texture packets, each texture packet being stored in the texture buffer and being associated with a texture map that is different than the texture maps associated with any other texture packet in the texture buffer, each texture packet including data relating to the location of its associated texture map in the texture buffer and data relating to the dimensional type of that texture packet's associated texture map;
wherein at least one of said texture processors is configured to convert the associated texture map to a one dimensional texture map by defining a plurality of data blocks within the texture map and then assigning a sequence number to each of the data blocks; and
wherein if said data blocks are consecutive in the texture map, they are stored next to each other in memory locations.

2 – 3. (Cancelled)

4. (Previously presented) The graphics accelerator as defined by Claim 1 wherein the dimensional type of each texture map is one of a one dimensional texture map, a two dimensional texture map, and a three dimensional texture map.
5. (Previously presented) The graphics accelerator as defined by Claim 1 wherein the texture processor further includes:
an input for receiving a texture message indicating that a texture map is to be utilized by the texture processor, the fetching engine responsively retrieving selected texture packets from the single texture buffer in response to receipt of the texture message.
6. (Original) The graphics accelerator as defined by Claim 5 wherein the texture processor further includes:
a parsing engine for parsing a fetched texture packet and determining information relating to the texture map associated with the fetched texture packet.
7. (Original) The graphics accelerator as defined by Claim 6 wherein the information relates to the location in the texture buffer of the texture map associated with the fetched texture packet.

8. (Original) The graphics accelerator as defined by Claim 6 wherein the information relates to the number of dimensions of the texture map associated with the fetched texture packet.

9-13. (Cancelled)

14. (Currently amended) A method of applying texture to a graphical image employing a graphics accelerator with a plurality of texture processors, the method comprising:

locating a texture packet identifying the location of a texture map in a single memory device, wherein the texture packet is associated with the texture map that is different than texture maps associated with other texture packets;

parsing the texture packet to determine the location of the texture map;

retrieving, based upon the determined location, the texture map from the single memory device;

(a) determining whether the texture map is one dimensional and reconstructing the texture map into one dimensional if the texture map is not one dimensional; and

(b) applying the resulted texture map from step (a) to the graphical image; and

~~reconstructing the texture map after it is retrieved from the single memory device; wherein the texture packet includes data relating to the dimensional type of the texture map, the texture map being reconstructed by parsing the texture packet to determine the dimensional type of the texture map, the texture map being~~

~~reconstructed based upon the determined dimensional type of the texture map.~~

15-19. (Cancelled)

20. (Currently Amended) A ~~computer program containing material device~~ containing a computer program for use on a computer system with a plurality of texture processors for applying texture to a graphical image, wherein said computer program comprising:

~~a computer program containing material having computer readable program code thereon, the computer readable program code including:~~

program code for locating a texture packet identifying the location of a texture map in a single memory device, wherein the texture packet is associated with the texture map that is different than texture maps associated with other texture packets;

program code for parsing the texture packet to determine the location and the number of dimensions of the texture map;

program code for retrieving, based upon the determined location, the texture map from the memory device;

program code for (a) determining whether said texture map is one dimensional and reconstructing said texture map into one dimensional if it is not one dimensional; and

program code for (b) applying the resulted texture map from step (a) to the graphical image; ~~and~~

~~program code for reconstructing the texture map after it is retrieved from the single memory device;~~

~~wherein the texture packet includes data relating to the dimensional type of the texture map, the program code for reconstructing comprising:
program code for parsing the texture packet to determine the dimensional type of the texture map, the texture map being reconstructed based upon the determined dimensional type of the texture map.~~

21-26. (Cancelled).

27. (Currently amended) A method of storing a texture map in a single linear texture memory of a graphics accelerator, the method comprising:

- A. determining the dimension of the texture map;
- B. converting the texture map to a one dimensional texture map if the dimension of the texture map is determined to be more than one dimensional, the one dimensional texture map having a first number of data blocks which are consecutive to each other;
- C. locating a second number of memory locations which are located next to each other in the single linear texture memory, the first number being equal to the second number; and
- D. storing the one dimensional texture map in the located memory locations in the single linear texture[[d]] memory;

wherein the texture map is two dimensional, step B comprising:

- B1. defining a plurality of data blocks within the texture map; and
- B2. assigning a sequence number to each of the data blocks, the sequence numbers being consecutive numbers; and

wherein step D comprises:

D1. consecutively storing each said data block of the one dimensional texture map in the located memory locations if said data blocks are consecutive to each other.

28-29. (Cancelled).

30. (Currently amended) A graphics accelerator for processing graphical request code, the graphics accelerator comprising:

a single linear texture memory for storing texture maps;

a plurality of texture processors that applies textures to items to be displayed, the plurality of the texture processors retrieving texture packets from the single linear texture memory, each texture processor including a texture map converter that converts texture maps having dimensions greater than one dimensional to a one dimensional texture map, each dimensional texture map having a first number of data blocks which can be consecutive to each other, the texture processor further including means for locating a second number of memory locations which are located next to each other in the single linear texture memory, the first number being equal to the second number; and

means for storing the one dimensional texture map in the located memory locations in the single texture memory;

wherein the texture map converter comprises:

means for defining a plurality of data blocks within the texture map; and

means for assigning a sequence number to each of the data blocks, the sequence numbers being consecutive numbers; and

the storing means comprises:

means for consecutively storing each data block of the one dimensional texture map in the located memory locations if said data blocks are consecutive to each other.

31-32. (Cancelled)

33. (Currently Amended) A device containing a computer program ~~computer program containing material —product~~ for use on a computer system for storing a texture map in a single linear texture memory of a graphics accelerator, wherein the computer program comprising:

~~a the computer program containing material product comprising a~~
~~computer usable medium having computer readable program code~~
~~thereon, the computer readable program code including:~~

program code for determining the dimension of the texture map;

program code for converting the texture map to a one dimensional texture map if the dimension of the texture map is determined to be more than one dimensional, the one dimensional texture map having a first number of data blocks which are consecutive to each other;

program code for locating a second number of memory locations which are next to each other in the single linear texture memory, the first number being equal to the second number; and

program code for storing the one dimensional texture map in the located memory locations in the single linear texture memory;

wherein the texture map is two dimensional, and the program code for converting comprises:

program code for defining a plurality of data blocks within the texture map; and

program code for assigning a sequence number to each of the data blocks, the sequence numbers being consecutive numbers; and

wherein the program code for storing comprises:
program code for consecutively storing each consecutive data block of
the one dimensional texture map in the located memory locations.

34-38. (Cancelled).